

Amendments to the Specification:

Please replace the paragraph, beginning at page 6, line 18, with the following rewritten paragraph:

B1
Figure 1 is a block diagram of a television receiver system that includes an exemplary embodiment of the invention. The television receiver includes a tuner 106 that is coupled to receive radio frequency input signals from an antenna 100 and/or a cable connection 102. The system also includes a satellite receiver 108 that receives television signals via a satellite dish 104 that are encoded according to the standard specified by the Moving Picture Experts Group (MPEG). Both the tuner 106 and the satellite receiver 108 are controlled by a processor 110. The exemplary tuner 106 may receive analog television signals or signals encoded according to the standard specified by the Advanced Television Systems Committee (ATSC). This standard is a subset of the MPEG standard. The tuner provides analog television signals to an analog television signal processor 112 and provides ATSC encoded television signals to an MPEG decoder 114. Other MPEG encoded signals provided, for example, by the satellite receiver 108 are also provided to the MPEG decoder 114. Both the analog television signal processor 112 and the MPEG decoder 114 provide analog video and audio output signals. The video output signals are applied to a video signal processor 116 while the audio signals are applied to an audio signal processor 118. The video signal processor 116 generates video images for display on a display device 120 while the audio signal processor 118 generates accompanying sounds through a speaker system 122.

Please replace the paragraph, beginning at page 7, line 11, with the following rewritten paragraph:

B2
The processor 110 receives remote control commands via a remote control receiver 124. This receiver may be a conventional infra-red or ultrasonic remote control receiver. The processor may also be coupled to an optional voice recognition system 126 (shown in phantom) that may be used to receive voice commands from a viewer, as described below. Finally, the exemplary television system includes an on-screen display memory 128 in which the processor 110 may build the menus and channel matrixes described below. The processor 110 is coupled to the video signal processor 116 to display these menus either on a blank screen or as an overlay on the video images that are currently being displayed.